

## AMENDMENTS TO THE CLAIMS

1. (Previously Amended) An integrated-circuit ("IC") layout comprising:
  - a) a net with routable elements;
  - b) a first set of interconnect lines for connecting the routable elements of the net, wherein the interconnect lines have ends that are in the shape of partial non-quadrilateral polygons.
2. (Currently Amended) The IC layout of claim 1, wherein the partial polygons are ~~is~~ a partial octagons.
3. (Currently Amended) The IC layout of claim 2, wherein the partial octagons have ~~has~~ several sides that are equal.
4. (Original) The IC layout of claim 3, wherein the partial octagons are half-octagons.
5. (Currently Amended) The IC layout of claim 1, wherein the partial polygons are ~~is a~~ partial hexagons.
6. (Currently Amended) The IC layout of claim 5, wherein the partial hexagons have ~~has~~ several equal sides.
7. (Original) The IC layout of claim 5, wherein the partial hexagons are half hexagons.
8. (Currently Amended) The IC layout of claim 1 further comprising a first set of vias that are in the shape of the non-quadrilateral polygons.
9. (Currently Amended) The IC layout of claim 8, wherein the polygons are ~~is an~~ octagons.

10. (Currently Amended) The IC layout of claim 9, wherein the octagons have ~~has~~ several sides that are equal.

11. (Original) The IC layout of claim 10, wherein the partial octagon ends of the interconnect lines are half octagons.

12. (Currently Amended) The IC layout of claim 8, wherein the polygons are ~~is a~~ hexagons.

13. (Currently Amended) The IC layout of claim 12, wherein the hexagons have ~~has~~ several equal sides.

14. (Original) The IC layout of claim 13, wherein the partial hexagon ends of the interconnect lines are half hexagons.

15. (Previously Amended) The IC layout of claim 1 further comprising a first set of vias that are in the shape of another non-quadrilateral polygon.

16. (Currently Amended) The IC layout of claim 1 further comprising a second set of interconnect lines that have ends that are in the shape of partial quadrilateral polygons.

17. (Original) The IC layout of claim 16, wherein the second set of interconnect lines have partial rectangular ends.

18. (Currently Amended) The IC layout of claim 16 further comprising a second set of vias that are in the shape of a quadrilateral polygons.